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Rce12818  
JTWIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Raul-Adrian Cernea et al.

Title: Non-Volatile Memory and Method With Bit Line to Bit Line Coupled Compensation

Application No.: 10/667,222 Filing Date: September 17, 2003

Examiner: Hoang, Huan Group Art Unit: 2818

Docket No.: SNDK.308US0 Conf. No.: 7021

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Mail Stop RCE  
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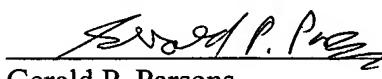
**REQUEST FOR CONTINUED EXAMINATION (RCE)**

Dear Sir:

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application. Please consider the Supplemental Information Disclosure Statement and Amendment, which are being filed herewith. The RCE fee required under 37 C.F.R. § 1.17(e) is authorized in an accompanying transmittal letter. Please contact the undersigned attorney with any questions concerning this request or the above-identified patent application.

<b>EXPRESS MAIL LABEL NO.:</b>
EV437669103US

Respectfully submitted,

  
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December 15, 2004

Date

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